

IN THE CLAIMS

Claims 19 - 40 have not been amended. They are in the same form as previously submitted in a July 9, 2003 Preliminary Amendment and are presented for the Examiner's convenience.

Claims 1 - 18 (cancelled).

19 (previously presented) A 10 Gigabit Attachment Unit Interface (XAUI) device comprising:

an output device capable of transmitting a serial data signal on a differential signaling pair over printed circuit board traces;

a plurality of current sources;

a plurality of selection devices coupled to the output device, each selection device being coupled to an associated one of the current sources to receive a first current when the selection device is active, each selection device being adapted to receive an associated bit of a parallel data signal; and

a current steering device to direct portions of a second current among a plurality of the current sources associated with inactive selection devices, the second current having a magnitude of about an integer multiple of a magnitude of the first current.

20. (previously presented) The XAUI device of claim 19, wherein the XAUI device comprises n current sources and n selection devices, and wherein the magnitude of the second current is about an $n-1$ multiple of the magnitude of the first current.

21 (previously presented) The XAUI device of claim 19, wherein the XAUI device comprises a plurality of output devices, each output device being capable of

transmitting a serial data signal on a differential signaling pair over printed circuit board traces.

22. (previously presented) The XAUI device of claim 19, wherein the parallel data signal comprises a ten-bit code group.

23. (previously presented) The XAUI device of claim 19, wherein the output device comprises circuitry to transmit the serial data signal on the differential signaling pair in response to bit signals transmitted by the selection devices.

24. (previously presented) The XAUI device of claim 19, wherein the output device is capable of transmitting the serial data signal on bit intervals, and wherein no more than one selection device is activated during a bit interval to transmit a bit signal to the output device.

25. (previously presented) The XAUI device of claim 24, wherein a selection device is activated in response to a clock signal.

26. (previously presented) The XAUI device of claim 19, wherein each selection device is coupled between an associated current source and the output device, and wherein the associated current source is activated to apply a current to the selection device contemporaneously with activation of the selection device.

27. (previously presented) The XAUI device of claim 19, the XAUI device further comprising a 10 gigabit media independent interface.

28. (previously presented) A. method comprising:
receiving one bit of a parallel data signal at each of a plurality of selection devices, each selection device being coupled to an associated one of a plurality of current sources;

applying a first current to an active selection device;

directing portions of a second current among a plurality of current sources coupled to inactive selection devices, the second current having a magnitude of about an integer multiple of a magnitude of the first current.

29. (previously presented) The method of claim 28, the method further comprising:

receiving one bit of the parallel data signal at each of n selection devices; and

directing the second current signal among $n-1$ current sources coupled to $n-1$ inactive selection devices,

wherein the magnitude of the second current is about an $n-1$ multiple of a magnitude of the first current.

30. (previously presented) The method of claim 28, wherein the parallel data signal comprises a ten-bit code group.

31. (previously presented) The method of claim 28, the method further comprising transmitting a serial data signal in response to bit signals transmitted by the selection devices.

32. (previously presented) The method of claim 31, wherein the bit signals are transmitted on bit intervals, and wherein the method further comprises activating no more than one selection device during a bit interval to transmit a bit signal.

33. (previously presented) The method of claim 28, the method further comprising activating a current source contemporaneously with activation of an associated selection device.

34. (previously presented) A parallel to serial conversion device comprising: an output device;

a plurality of current sources;

a plurality of selection devices coupled to the output device, each selection device being coupled to an associated one of the current sources to receive a first current when the selection device is active, each selection device being adapted to receive an associated bit of a parallel data signal;

a current steering device to direct portions of a second current among a plurality of the current sources associated with inactive selection devices, the second current having a magnitude of about an integer multiple of a magnitude of the first current.

35. (previously presented) The parallel to serial conversion device of claim 34, wherein the parallel to serial conversion device comprises n current sources and n selection devices, and wherein the magnitude of the second current is about an $n - 1$ multiple of the magnitude of the first current.

36. (previously presented) The parallel to serial conversion device of claim 34, wherein the parallel data signal comprises a ten-bit code group.

37. (previously presented) The parallel to serial conversion device of claim 34, wherein the output device comprises circuitry to transmit a serial data signal on a differential signaling pair in response to bit signals transmitted by the selection devices.

38. (previously presented) The parallel to serial conversion device of claim 34, wherein the output device is capable of transmitting a serial data signal on bit intervals, and wherein no more than one selection device is activated during a bit interval to transmit a bit signal to the output device.

39. (previously presented) The parallel to serial conversion device of claim 38, wherein a selection device is activated in response to a clock signal.

40. (previously presented) The parallel to serial conversion device of claim 34, wherein each selection device is coupled between an associated current source and the output device, and wherein the associated current source is activated to transmit a current to the selection device contemporaneously with activation of the selection device.